Miscellaneous Network Note #23

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27 March 72

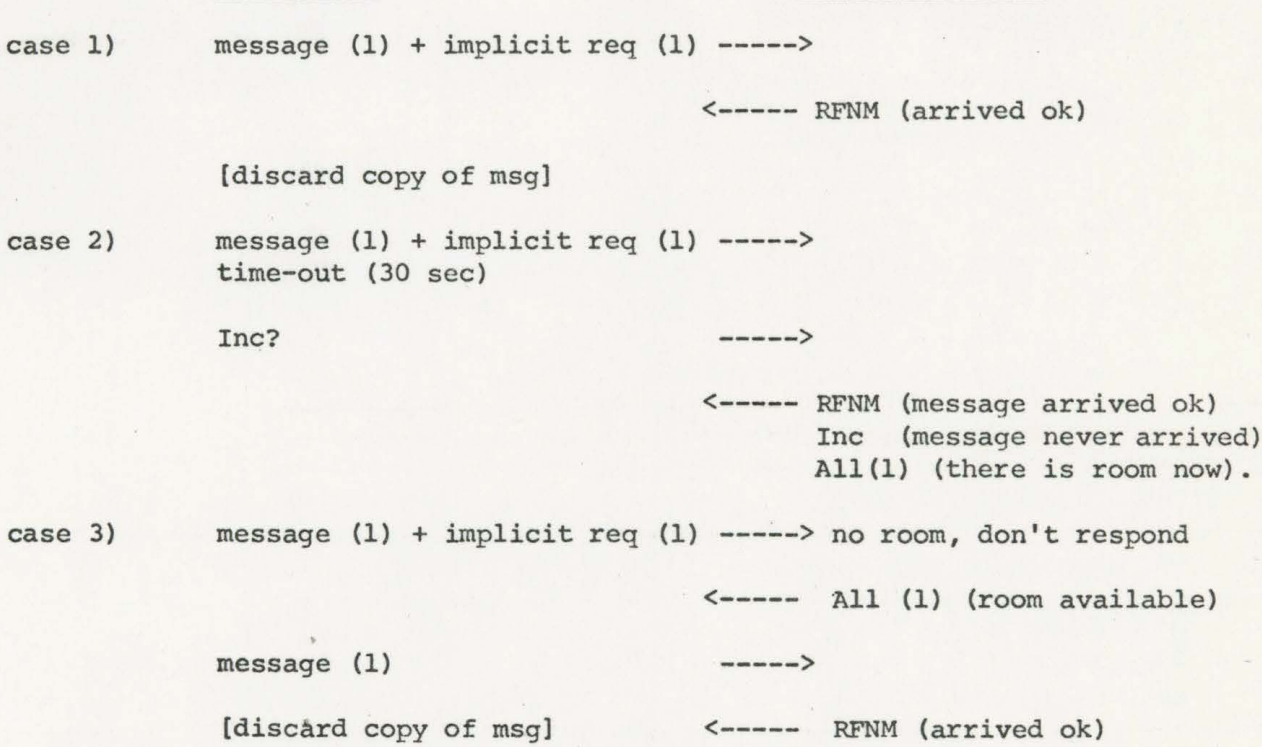
The new flow-control scheme for IMPSYS

BB&N report number 2161 outlines part of the new scheme for IMPSYS flow control to be installed sometime in the second quarter, 1972. A meeting held at BB&N March 16, 1972 was devoted to the description of this new scheme for the benefit of interested network participants.

This note represents my understanding of the new flow control mechanisms. The essential goal is to eliminate unnecessary retransmissions, prevent re-assembly lock-up, and control traffic from HOSTs into the net more strictly than the earlier link blocking method.

Source IMP-to-Destination IMP Protocol

There are two different protocols depending on message length (i.e. single or multi-packet). We illustrate first the single packet case.



In the first case, a single packet message is sent to the destination IMP. This message acts as an implicit request for single packet buffer space. If there is room, as in case 1, the destination IMP responds with

a RFNM. The source IMP, which has retained a copy of the message, deletes its copy and goes on.

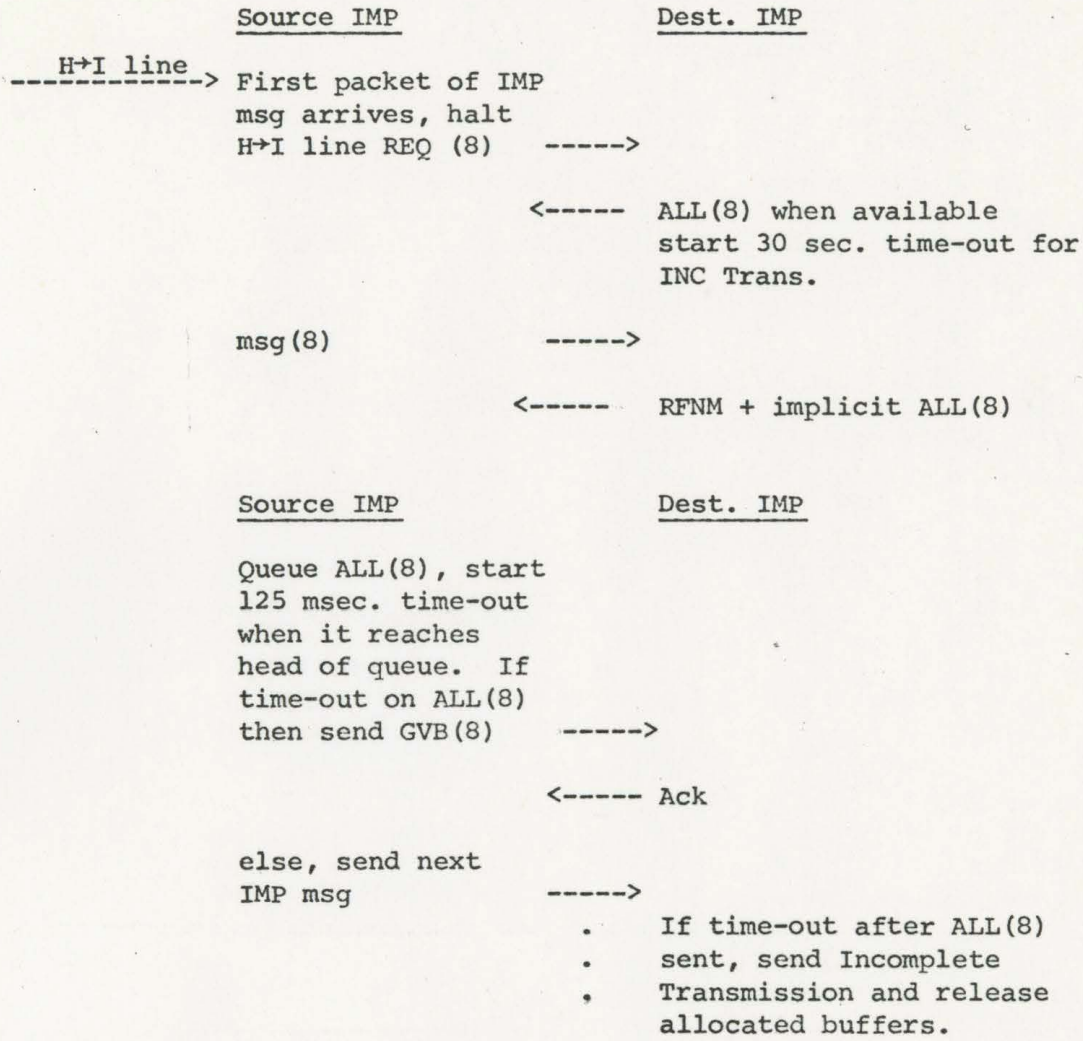
In the second case, the message is sent to the destination IMP and either never arrives, or arrives and there was no room, or arrives and is accepted, but the RFNM got lost. In this case, the source IMP, still retaining its copy of the message, times out after 30 seconds and sends an INC? message to the destination IMP. This message contains enough data to uniquely identify the message about which the source IMP is inquiring. The destination IMP may respond with a RFNM if the message arrived safely, or with INC if the message did not arrive or with ALL (1) if there is now room for a one packet message and it is requesting that this message be sent now.

The third case illustrates what happens when the source IMP sends a message to a destination IMP at which there is no room for the one-packet message. The arrival of the single packet message constitutes a request for single packet buffer space, and is recorded as such by the destination IMP in a first-come-first-served buffer reservation request queue. When space is available, the destination IMP will transmit an ALL (1) to the requesting source IMP which can then send the single packet message again, this time knowing that space has been reserved at the destination.

Two important points to notice are that links no longer play a part in the IMP flow-control mechanism, and that at most only four messages in each direction can be in transit between any pair of IMP sites.

For multi-packet messages, the procedure is somewhat different. When a message enters an IMP from a HOST, and the "last bit" flag is not set when the number of bits in a maximum length single packet have arrived, the IMP halts the HOST-IMP transmission line while it determines whether space has been reserved at the dest. IMP. If space (8 packets worth) has been reserved, the HOST-IMP line is re-opened, and the message is sent out normally. If space has not been reserved, the HOST-IMP line is kept closed while the source IMP makes a request for multi-packet buffer storage at the destination IMP. When 8 buffers are available, the destination IMP responds with an ALL (8). The source IMP then transmits the message, and waits for a combination RFNM and ALL (8) from the destination IMP. The destination IMP will delay its RFNM until it has another 8 buffers available for the next multipacket message.

This sequence is illustrated below:


Source IMP
Dest. IMP
H→I line
First packet of IMP msg arrives, halt H→I line REQ (8)
ALL(8) when available start 30 sec. time-out for INC Trans.
msg(8)
RFNM + implicit ALL(8)
Source IMP
Dest. IMP
Queue ALL(8), start 125 msec. time-out when it reaches head of queue. If time-out on ALL(8) then send GVB(8)
Ack
else, send next IMP msg
If time-out after ALL(8) sent, send Incomplete Transmission and release allocated buffers.

A key point in this protocol is that a source IMP, after receipt of a RFNM and implicit ALL(8) from the destination IMP, has 125 msec. in which to complete the transfer of at least the first packet of a multi-packet message to the destination IMP. The source IMP may have several allocate responses queued up in which case these time-outs occur one after the other (one has to time-out before the next 125 msec time-out starts).

As before, time-outs exists in the source IMP which cause it to send INC? messages to the destination IMP if it has received no response from some earlier message.

Buffer Allocation

A total of 40 buffers are available for store/forward and re-assembly purposes. At most 32 can be allocated for re-assembly, and at most 24-25 can be allocated for store and forward use. This prevents either kind of traffic from completely shutting out the other kind.

Message Ordering (Source IMP-to-Destination IMP).

IMPs mark messages leaving for other destinations with an 8-bit message number. In addition, a 2-bit priority number is also used in case a HOST has marked a message as a priority message. The key notion here is that the IMPs treat all HOSTs on a given IMP as if they were a single HOST. A single sequence of message and priority numbers is used between each pair of sites.

The receiving IMP remembers the message number of the last message delivered, as well as the priority number of the last priority message delivered. It uses this information to correctly sequence messages out the IMP-HOST line (s). Since there is only one sequence of numbers for each pair of sites, messages for one HOST at a site may get in the way of messages for another HOST at the same site. In fact, if some message, m, is the next in line to go to some HOST, and that HOST delays receipt for 30 seconds, any messages for another HOST may be delayed that long also. However, only the first message is lost, since the second one could not even start into its destination HOST until the first one had been delivered. There is a tighter coupling between HOSTs sharing an IMP than before, but not much tighter.

An example of the use of message and priority numbers is given below.

Order sent by Source IMP		Order sent by Dest. IMP		Order sent by HOST
11, $12P_1$, $13P_2$, 14	-------->	$13P_2$, $12P_1$, 14, 11	-------->	$12P_1$, $13P_2$, 11, 14
11, $12P_1$, $13P_2$, 14	-------->	$13P_2$, 11, 14, $12P_1$	-------->	11, 12 P_1, $13P_2$, 14

where $13P_2$ is interpreted to mean
message #13, priority number ?.

Note that there are only 2 classes of messages, priority and non-priority, and that the priority numbers simply allow ordering at the destination of multiple outstanding transmissions from the same site.

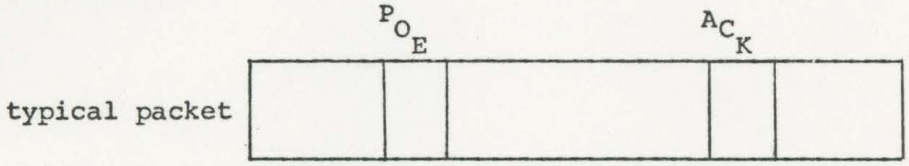
If HOSTs use link numbers to de-multiplex messages to processes, then it would be a mistake to arbitrarily assign short messages priority. If a file transmission were carried out such that the last short message had priority, the file might not enter the receiving HOST in the same order it was sent!

ACK Mechanism

IMPs treat their physical channels (phone lines) as if they were pairs of simplex communications paths. Each IMPSYS has a sender and receiver module for each full duplex channel. Each module has an "ODD/EVEN" bit which is used to keep track of the state of the last packet on the line. The object is for the sender module to "block" a channel until the corresponding receiver has received a packet indicating that the sent packet was received on the other end (i.e. an acknowledgment).

In the present system, acknowledgments are separate IMP-IMP packets. In the new system, they are a single bit in a packet flowing in the opposite direction on the reverse path of a full duplex channel.

Every packet sent between IMPs has an ACK bit and an OE bit, as shown below.



We need some terminology: Let POE be the packet OE bit, and SOE, ROE be the send module OE bit and Receive module OE bit respectively. For two IMPS, A and B, we distinguish SOE/A and SOE/B as the two send module OE bits at IMPs A and B respectively.

The rules of operation are as follows:

Sender

if ACK ≠ SOE then do nothing

else SOE ← $\overline{SOE}$ (i.e. flip SOE bit) and free channel.

Receiver

if POE = ROE then lost last packet, so send it again without changing ROE

else ROE ← $\overline{ROE}$ and send ACK in a new packet when it comes along.

Whenever a packet is sent by the send module, its two bits, POE and ACK are set up by:

POE ← SOE
ACK ← ROE

The mechanism is designed to use real traffic to accomplish the acknowledgment protocol by piggy-backing the ACK bits in the header of real packets. If there is no real packet waiting for transmission in the opposite direction, a fake packet is assembled which carries the ACK, but which is not acknowledged by the receiving side.

We give an example of the operation of this mechanism between two IMPS.

IMP A	ROE	SOE		ROE	SOE	IMP B
sender blocks channel.	1	0	① POE 0 ACK 1 →	1	0	sender NOPS. rec. flips ROE
sender frees channel, Flips SOE	0	1	← POE 0 ACK 0 ②	0	0	sender blocks channel for new traffic
sender blocks channel			③ POE 1 ACK 0 → crashes or gets lost			
Receiver detects packet lost (POE=ROE) sender does not change OE bit.			← POE 0 ACK 0 ②			sender retransmits since no ACK received
sender retransmits packet ③			③ POE 1 ACK 0 →	1	1	sender flips SOE unblocks channel
						receiver flips ROE
receiver flips ROE sender flips SOE	1	0	← POE 1 ACK 1 ④			

In fact such send/receive module has 8 OE bits, so up to 8 packets can be outstanding in either direction.

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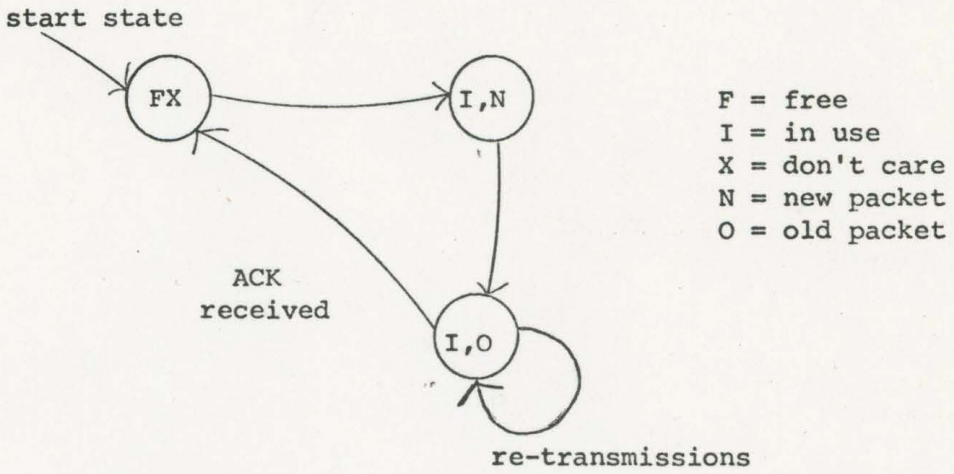
How things really work

Actually, a single send module is responsible for trying to transmit packets out on the 8 pseudo-channels. Each channel has a two bit state (in addition to an OE bit). Each channel is either FREE or IN USE and if IN USE, it may be sending OLD or NEW packet.



Between IMPs, packets are sent repeatedly, until they are acknowledged. However, the choice of what to send next is ordered by priority as follows:

1. Priority Packets (as marked by HOST)
2. Non-Priority Packet
3. Unacknowledged packets (on I,O state channels)
4. Others

It was pointed out that a heavy load of type (1) and (2) traffic might prevent retransmissions from occurring at all, and W. Crowther responded that the bug would be fixed (probably by a sort of delay dependent priority or Time-out scheme).